

Electronic Acknowledgement Receipt

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Application Number:	09710057
Patent Number:	6871298
Confirmation Number:	4116
Petition Issued Date:	June 25,2009
Title of Invention:	METHOD AND APPARATUS THAT SIMULATES THE EXECUTION OF PARALLELED INSTRUCTIONS IN PROCESSOR FUNCTIONAL VERIFICATION TESTING
First Named Inventor/Applicant Name:	Becky Cavanaugh
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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Petition automatically granted by EFS	sb0066e_fill.pdf	968091 e87ca3ed58b4b2087554624d36137b5dc914789	no	3

Warnings:

Information:

2	Fee Worksheet (PTO-875)	fee-info.pdf	32249 8e631bedc171935c473d45bc19650631bc0767c	no	2
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

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New International Application Filed with the USPTO as a Receiving Office

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